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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10018795	12/21/2001	257	347	2811	HW

**APPLICANTS: Nanno Yutaka; Okada Takashi; Yamano Atsunori; Senda Kouji;

**CONTINUING DATA VERIFIED:

THIS APPLICATION IS A 371 OF PCT/JP00/06330 09/14/2000

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** FOREIGN APPLICATIONS VERIFIED:

JAPAN 2000-131264 04/28/2000

JAPAN 2000-197536 06/30/2000

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		OGOH:104	
Verified and Acknowledged Examiners's initials			
TITLE : Thin film transistor and method for fabricating the same, and liquid crystal display comprising the same			

U.S. DEPT. OF COMM./PAT.: TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner PREPARED FOR ISSUE Application Examiner	
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